1) What problems would you predict with moving whole segments into and out of memory?

2) Assuming a page size of 1024 bytes, complete the Page Tables for the pages in memory, and determine the physical address for the logical address <2, 1032>.

```
CPU

Running Process X

Segment Table for X

Valid Bit

Frame# 0 1 2 3

Pointer to Page Table

Physical Memory

Frame Number 0 1 2 3 4 5 6

Page Table for Seg. 0

Valid Bit

Frame# 0 1 2 3

Page Table for Seg. 1

Valid Bit

Frame# 0 1 2 3

Page Table for Seg. 2

Valid Bit

Frame# 0 1 2 3

Page Table for Seg. 3

Valid Bit

Frame# 0 1 2 3

Physical Addr.

frame# offset

Logical Addr.
```