

## Computer Architecture Fall 2009

Lect #	Tuesday		Thursday	
1	8/25	Ch. 2: High-level and Assembly Language Review	8/27	Ch 10 & 11: Instruction Sets
3	9/1	Ch. 12: Instruction Pipelining	9/3	Ch 12: Pipeline Data Hazards and Forwarding
5	9/8	Ch 12: Pipeline Branch Hazards	9/10	Ch 13: RISC vs. CISC
7	9/15	Ch 14: Instruction-Level Parallelism (ILP) and Superscalar Processors	9/17	
9	9/22	Ch 14: ARM Cortex-A8 Processor	9/24	
11	9/29	Review for Test 1	10/1	<b>Test 1</b>
13	10/6	Ch 3: Bus Design Issues	10/8	PCI Bus Protocol
15	10/13	Ch 5: Memory Hierarchy	10/15	
17	10/20	Ch 4: Cache Memory	10/22	Ch 5: RAM/Main Memory
19	10/27	Ch 6: Magnetic Disks and RAID	10/29	Ch 7: I/O: memory-mapped vs isolated I/O; programmed-I/O, interrupt-driven, and DMA
21	11/3	Review for Test 2	11/5	<b>Test 2</b>
23	11/10	Ch: 8: Operating System Support	11/12	Ch 8: Virtual Memory
25	11/17	Ch 8: Page replacement, page allocation, and segmentation	11/19	Ch 17: Parallel Processing
<b>Thanksgiving Break: Nov. 23 - 27</b>				
27	12/1		12/3	Ch. 18: Multicore Computers
29	12/8		12/10	Review for Final
<b>Final:</b> Thursday, December 17 from 10-11:50 AM in ITT 328				