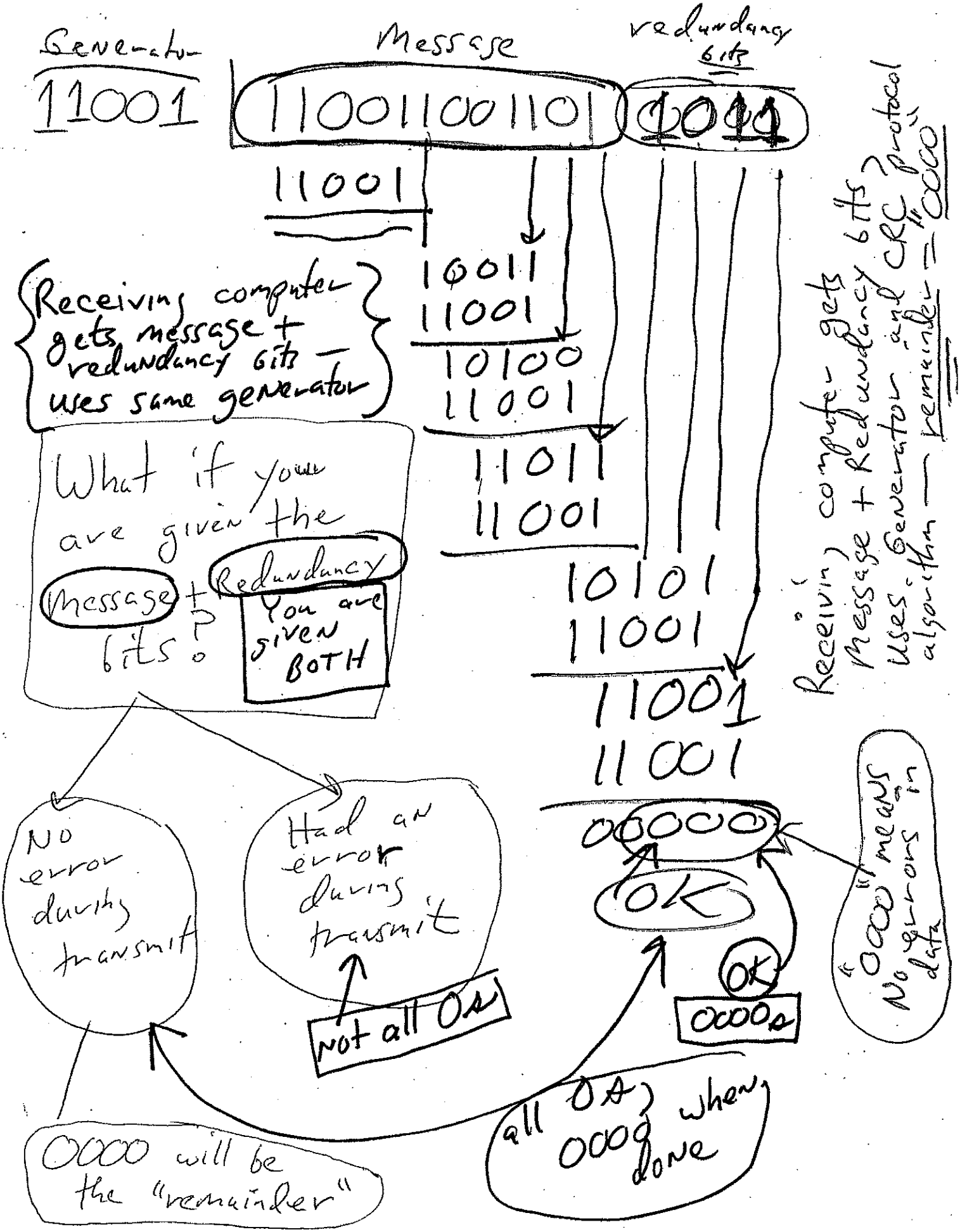

Generator

11001

Message

11001100110 1

redundancy bits

1011

11001

10011
11001
10100
11001
11011
11001
10101
11001
11001
11001
00000

OK

OK

0000s

Receiving computer gets message + redundancy bits — uses same generator

What if you are given the message + redundancy bits?

You are given BOTH

No error during transmit

Had an error during transmit

not all 0s

0000 will be the "remainder"

all 0s, 0000 when done

"0000" means No errors in data

Receiving computer gets message + Redundancy bits, uses Generator and CRC protocol algorithm — remainder = "0000"